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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Li et al.
Serial No. : 10/729,859
Filed : December 5, 2003
Title : MAGNETIC FIELD EFFECT TRANSISTOR, LATCH AND METHOD
Docket : NAY 0001 PA/31558.2
Art Unit : 2811

CERTIFICATE OF MAILING

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James E. Beyer Reg. No. 39,564

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

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UNDER 37 CFR §§ 1.56, 1.97, AND 1.98

Applicants submit herewith patents, publications, and other information of which they are aware, which they believe may be material, as defined in 37 CFR §1.56(b), to the examination of this application and in respect of which there may be a duty to disclose in accordance with 37 CFR §1.56(a). While the information referred to in this Information Disclosure Statement may be material pursuant to 37 CFR §1.56(b), the filing of this Information Disclosure Statement is not intended to, pursuant to 37 CFR §1.97(h), constitute an admission that any patent, publication, or other information referred to is, or is considered to be, material to the patentability of this invention. No representation is made that a reference is "prior art" within the meaning of 35 U.S.C. §§ 102 and 103, and Applicants reserve the right, pursuant to 37 C.F.R. § 1.131 or otherwise, to establish otherwise. Further, pursuant to 37 CFR §1.97(g), the filing of this Statement should not be construed as a statement that a search has been made or that no other material information exists.


Serial No. 10/729,859
Docket No. NAY 0001 PA/31558.2

This Information Disclosure Statement is being filed within the period set forth in 37 CFR §1.97(b) because it is believed to be filed before the mailing date of a first office action on the merits.

The Office has waived the requirement pursuant to 37 CFR 1.98 (a)(2)(i) for submitting a copy of each cited U.S. patent and each U.S. patent application publication for all U.S. national patent applications filed after June 30, 2003 and for all international applications that have entered the national stage under 35 USC § 371 after June 30, 2003. Therefore, no copies of each cited U.S. patent and each cited U.S. patent application publication are enclosed, but the cited U.S. patents and the cited U.S. patent application publications are listed on PTO/SB/08A.

Respectfully submitted,
DINSMORE & SHOHL LLP

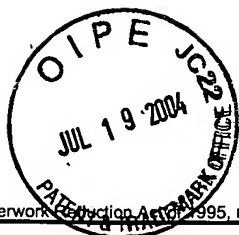
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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

Application Number	10/729,859
Filing Date	December 5, 2003
First Named Inventor	LI, Zhiqing
Art Unit	2811
Examiner Name	Not assigned yet
Attorney Docket Number	NAY 0001 PA/31558.2

Sheet 2 of 2

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		STEFAN ROHRER et al., "A MAGFET Sensor Array for Digital Magnetic Signal Reading", IEEE, 1997, p. 308-310	
		F.J. KUB et al., "Multiple-Gate Split-Drain MOSFET Magnetic-Field Sensing Device and Amplifier", IEDM 1992, p. 517-20.	
		LI, Z. et al., "A CMOS Magnetic Latch with Extremely High Resolution", IEDM 2002, Tech. Digest, pp. 909-12.	
		LI, Z. et al., "Weak Magnetic Field Pattern Detection by CMOS Magnetic Latch", IEEE, 2003.	

Examiner Signature	Date Considered
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